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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/988,593 11/20/2001
TWI

** FOREIGN APPLICATIONS *****

JAPAN P2001-001418 01/09/2001
TWI

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/28/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 34	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature <i>John Hanch</i>	Initials <i>JH</i>				

ADDRESS

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*WPI 05/05
TITLE Method of Manufacturing*

Semiconductor device having impurity region under isolation region

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